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APPLICANTS

Hiroshi Akasaki, Ome, JAPAN;
Shuichi Miyaoka, Hannou, JAPAN;
Yuji Yokoyama, Ome, JAPAN;
Masatoshi Hasegawa, Ome, JAPAN;
Kozaburo Kurita, Ome, JAPAN;

Y/VTN

** CONTINUING DATA *****

This application is a DIV of 10/153,525 05/24/2002 PAT 6,735,129

** FOREIGN APPLICATIONS *****

JAPAN 2001-158449 05/28/2001

Y/VTN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/25/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				

Verified and
Acknowledged

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Examiner's Signature
Initials

ADDRESS

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TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PROGRAMMABLE DELAYS FOR GENERATING
TIMING SIGNALS WITH TIME DIFFERENCE BEING NON-INTEGRAL MULTIPLE OF CLOCK CYCLE

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
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